

Research-oriented gPBL between SU

Date	Place	Partner Organization	Students' Major and Grade	Participants' Information	SIT Instructor
2025/06/10 ~2025/06/18	Japan	Silpakorn University	<ul style="list-style-type: none"> Department of Electronic Engineering, Advanced Electronic Engineering Undergraduate 1st~4th grade 	(SIT) Students 9, Student Staff 5, Professor 3 (Silpakorn University) Students 12, Professor 2	ISHIKAWA Hiroyasu (Electrical and Electronic Engineering Advanced Electronic Engineering), KOIKE Yoshikazu (Electrical and Electronic Engineering Advanced Electronic Engineering), NAKANO Masaki (Electrical and Electronic Engineering Advanced Electronic Engineering)



In the Semiconductor Electronics Laboratory, Superstructured Quantum Materials Electronics Laboratory, and Electro-Mechanical Systems Laboratory in Department of Electronic Engineering (Advanced Electronic Engineering Course) at SIT, teams consisting of 3 SIT students and 4 SU students worked on their research projects. Following a brief lecture on research activities, the students learned the utilization of facilities and equipment in each laboratory. They engaged in tasks such as thin-film fabrication and evaluation, microcomputer setup and measurement, and discussion of results. During the final presentation, they summarized and shared the outcomes of their research projects. Subsequently, through a question-and-answer session involving participating students and teachers, they deepened their understanding of the results and discussions.

